FIG.1

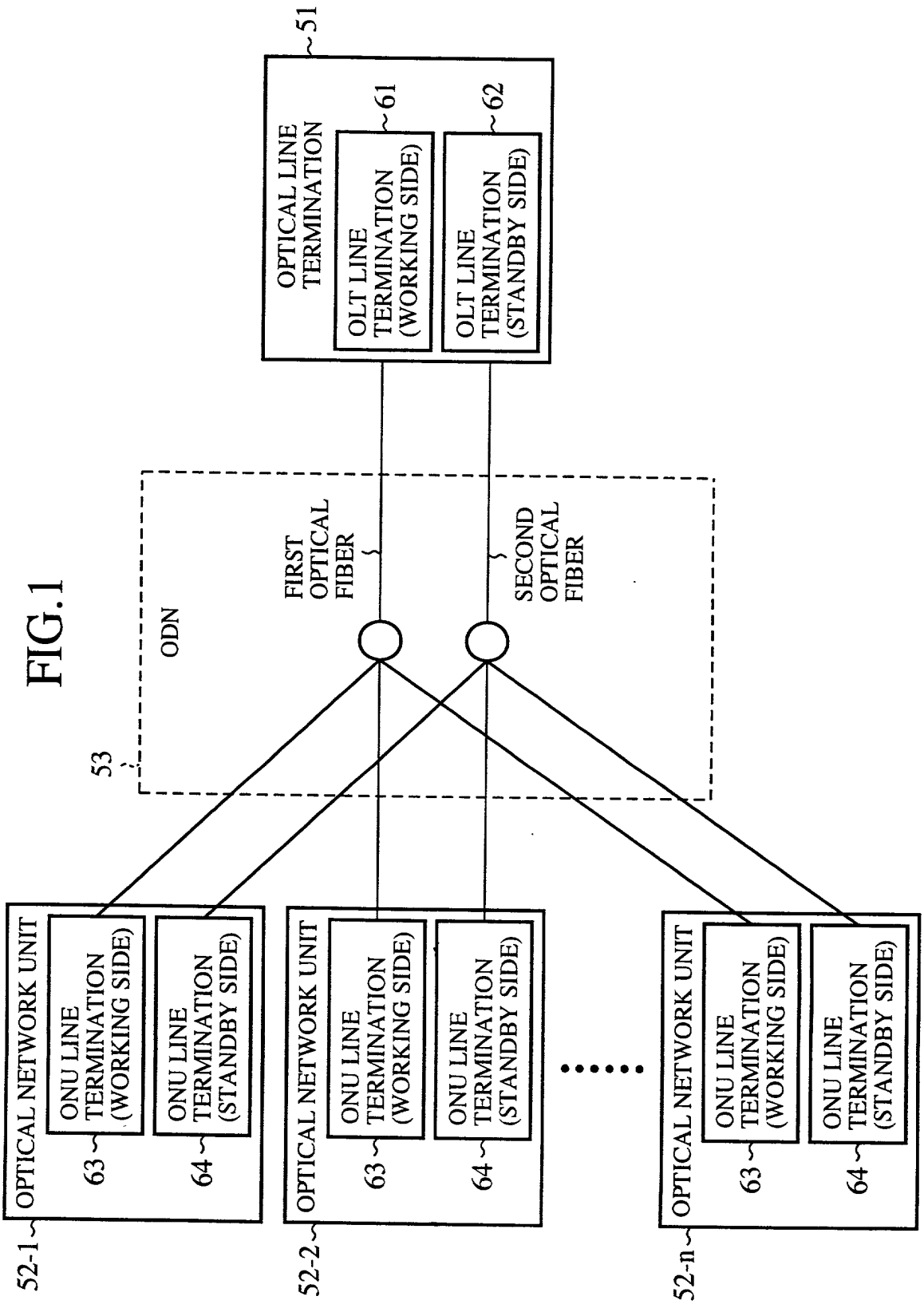
51
OPTICAL LINE TERMINATION
61
OLT LINE TERMINATION (WORKING SIDE)
62
OLT LINE TERMINATION (STANDBY SIDE)
FIRST OPTICAL FIBER
SECOND OPTICAL FIBER
53
ODN
OPTICAL NETWORK UNIT
ONU LINE TERMINATION (WORKING SIDE)
63
ONU LINE TERMINATION (STANDBY SIDE)
64
52-1
52-2
52-n

FIG.2

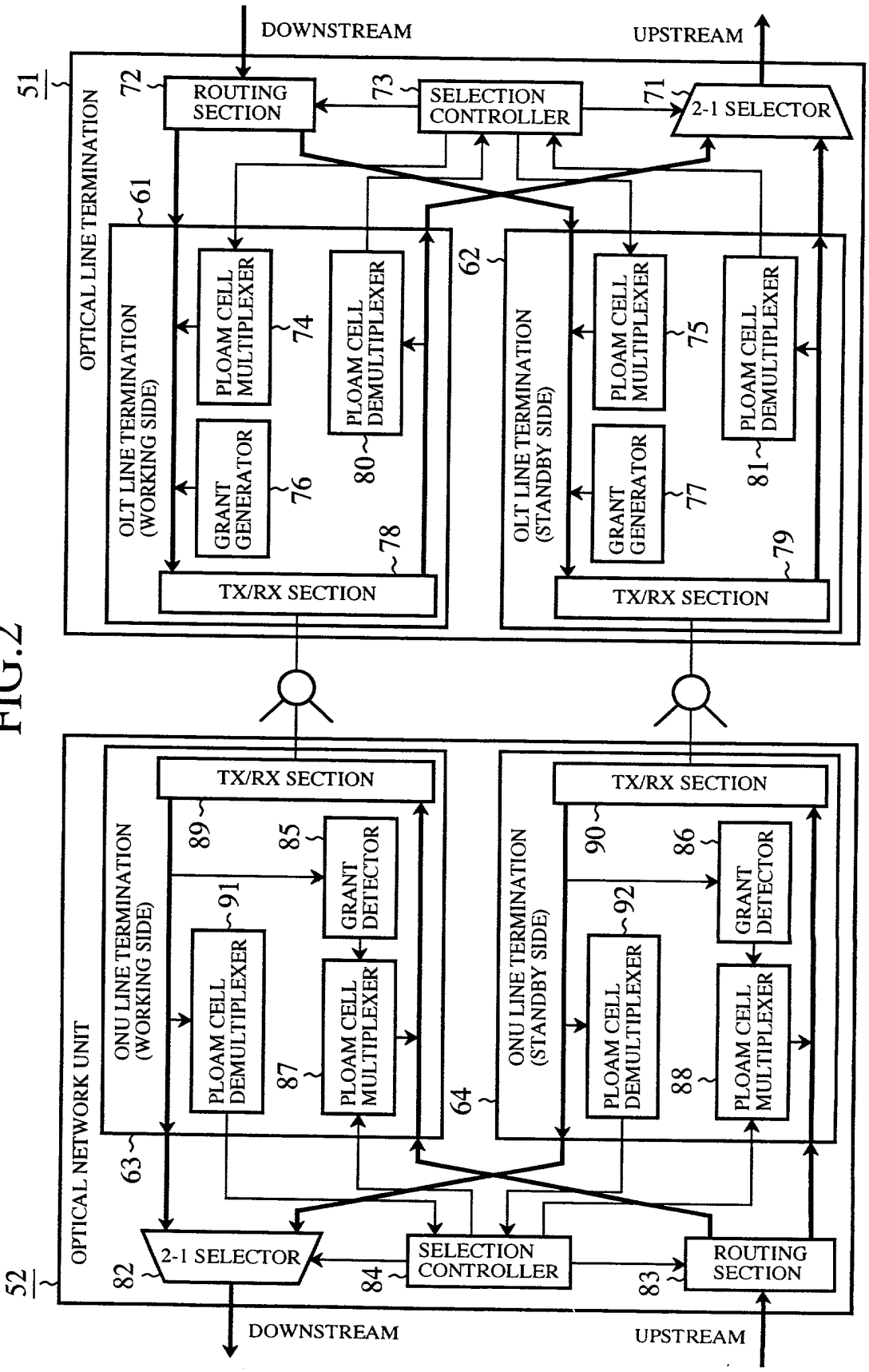
DOWNSTREAM
UPSTREAM
51
OPTICAL LINE TERMINATION
72
ROUTING SECTION
73
SELECTION CONTROLLER
71
2-1 SELECTOR
61
OLT LINE TERMINATION (WORKING SIDE)
PLOAM CELL MULTIPLEXER
74
PLOAM CELL DEMULTIPLEXER
GRANT GENERATOR
76
80
78
TX/RX SECTION
62
OLT LINE TERMINATION (STANDBY SIDE)
PLOAM CELL MULTIPLEXER
75
PLOAM CELL DEMULTIPLEXER
GRANT GENERATOR
77
81
79
TX/RX SECTION
52
OPTICAL NETWORK UNIT
ONU LINE TERMINATION (WORKING SIDE)
TX/RX SECTION
89
85
GRANT DETECTOR
91
PLOAM CELL DEMULTIPLEXER
87
PLOAM CELL MULTIPLEXER
63
64
ONU LINE TERMINATION (STANDBY SIDE)
TX/RX SECTION
90
86
GRANT DETECTOR
92
PLOAM CELL DEMULTIPLEXER
88
PLOAM CELL MULTIPLEXER
82
2-1 SELECTOR
84
SELECTION CONTROLLER
83
ROUTING SECTION
DOWNSTREAM
UPSTREAM

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FIG.3

CONTROL EXAMPLE (NON-REVERTIVE MODE)

FAULT STATE	ONU TO OLT		OLT TO ONU		OPERATION		
	K1 BYTE	K2 BYTE	K1 BYTE	K2 BYTE	ONU	OLT	
NO FAILURE	NO SW REQ.	(ONU IS) SELECTING WS	NO SW REQ.	(OLT IS) SELECTING WS	SL IS OPERATING IN WS	SL IS WORKING AT WS	~S11
EQUIPMENT FAILURE OCCURS IN WS TX/RX OF ONU	SW REQ. BY WORKING SF	(ONU IS) SELECTING WS	NO SW REQ.	(OLT IS) SELECTING WS	DETECT SW REQ. BY WORKING SF; SL IS WORKING AT WS; UPDATE T-K1 BYTE	SL IS WORKING AT WS	~S12
	SW REQ. BY WORKING SF	(ONU IS) SELECTING WS	NO SW REQ.	(OLT IS) SELECTING SS	DETECT SW REQ. BY WORKING SF; SL IS WORKING AT WS; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO SS; UPDATE T-K2 BYTE	~S13
	SW REQ. BY WORKING SF	(ONU IS) SELECTING SS	NO SW REQ.	(OLT IS) SELECTING SS	DETECT RR BY RECEIVING K2; SL IS SWITCHED TO SS; UPDATE T-K2 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO SS; UPDATE T-K2 BYTE	~S14
EQUIPMENT FAILURE IS RESTORED IN WS TX/RX OF ONU	DO NOT REVERT TO WS	(ONU IS) SELECTING SS	NO SW REQ.	(OLT IS) SELECTING SS	DETECT SW REQ. CLEAR; DO NOT REVERT STATE; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO SS; UPDATE T-K2 BYTE	~S15
SIGNAL DEGRADE OCCURS IN SS TX/RX OF ONU	SW REQ. BY STANDBY SD	(ONU IS) SELECTING SS	NO SW REQ.	(OLT IS) SELECTING SS	DETECT SW REQ. BY WS SD; SL IS OPERATING AT SS; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO SS; UPDATE T-K2 BYTE	~S16
	SW REQ. BY STANDBY SD	(ONU IS) SELECTING SS	NO SW REQ.	(OLT IS) SELECTING WS	DETECT SW REQ. BY WS SD; SL IS OPERATING AT SS; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO WS; UPDATE T-K2 BYTE	~S17

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FIG.4

SIGNAL DEGRADE OCCURS IN SS TX/RX OF ONU	SW REQ. BY STANDBY SD	(ONU IS) SELECTING WS	NO SW REQ.	(OLT IS) SELECTING WS	DETECT RR BY RECEIVING K2; SL IS SWITCHED TO WS; UPDATE T-K2 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO WS; UPDATE T-K2 BYTE	~S18
SIGNAL DEGRADE IS RESTORED IN SS TX/RX OF ONU	NO SW REQ.	(ONU IS) SELECTING WS	NO SW REQ.	(OLT IS) SELECTING WS	NO REQ.; UPDATE K1 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO WS; UPDATE T-K2 BYTE	~S19

NOTES: WS = WORKING SIDE
SS = STANDBY SIDE
TX/RX = TRANSMITTING AND RECEIVING SECTION
ONU = OPTICAL NETWORK UNIT
OLT = OPTICAL LINE TERMINATION
SW = SWITCH OR SWITCHING
REQ. = REQUEST
SF = SIGNAL FAIL
SD = SIGNAL DEGRADE
T-K1 = TRANSMISSION K1 BYTE
T-K2 = TRANSMISSION K2 BYTE
RR = REMOTE REQUEST
SL = SELECTOR

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FIG.5

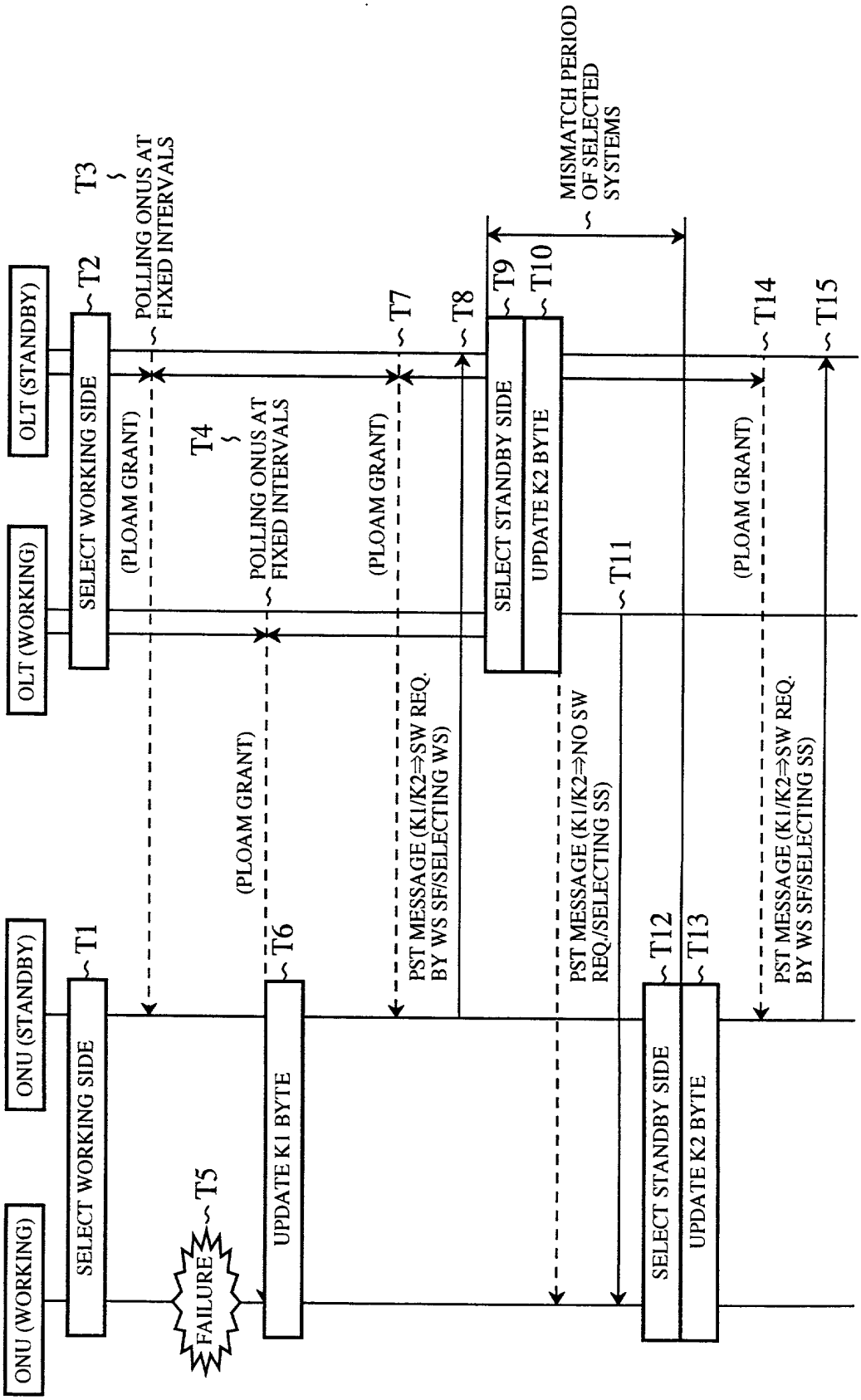
ONU (WORKING)
ONU (STANDBY)
OLT (WORKING)
OLT (STANDBY)
SELECT WORKING SIDE
SELECT STANDBY SIDE
UPDATE K1 BYTE
UPDATE K2 BYTE
FAILURE
T1
T2
T3
T4
T5
T6
T7
T8
T9
T10
T11
T12
T13
T14
T15
POLLING ONUS AT FIXED INTERVALS
(PLOAM GRANT)
PST MESSAGE (K1/K2⇒SW REQ. BY WS SF/SELECTING WS)
PST MESSAGE (K1/K2⇒NO SW REQ./SELECTING SS)
PST MESSAGE (K1/K2⇒SW REQ. BY WS SF/SELECTING SS)
MISMATCH PERIOD OF SELECTED SYSTEMS

FIG.6

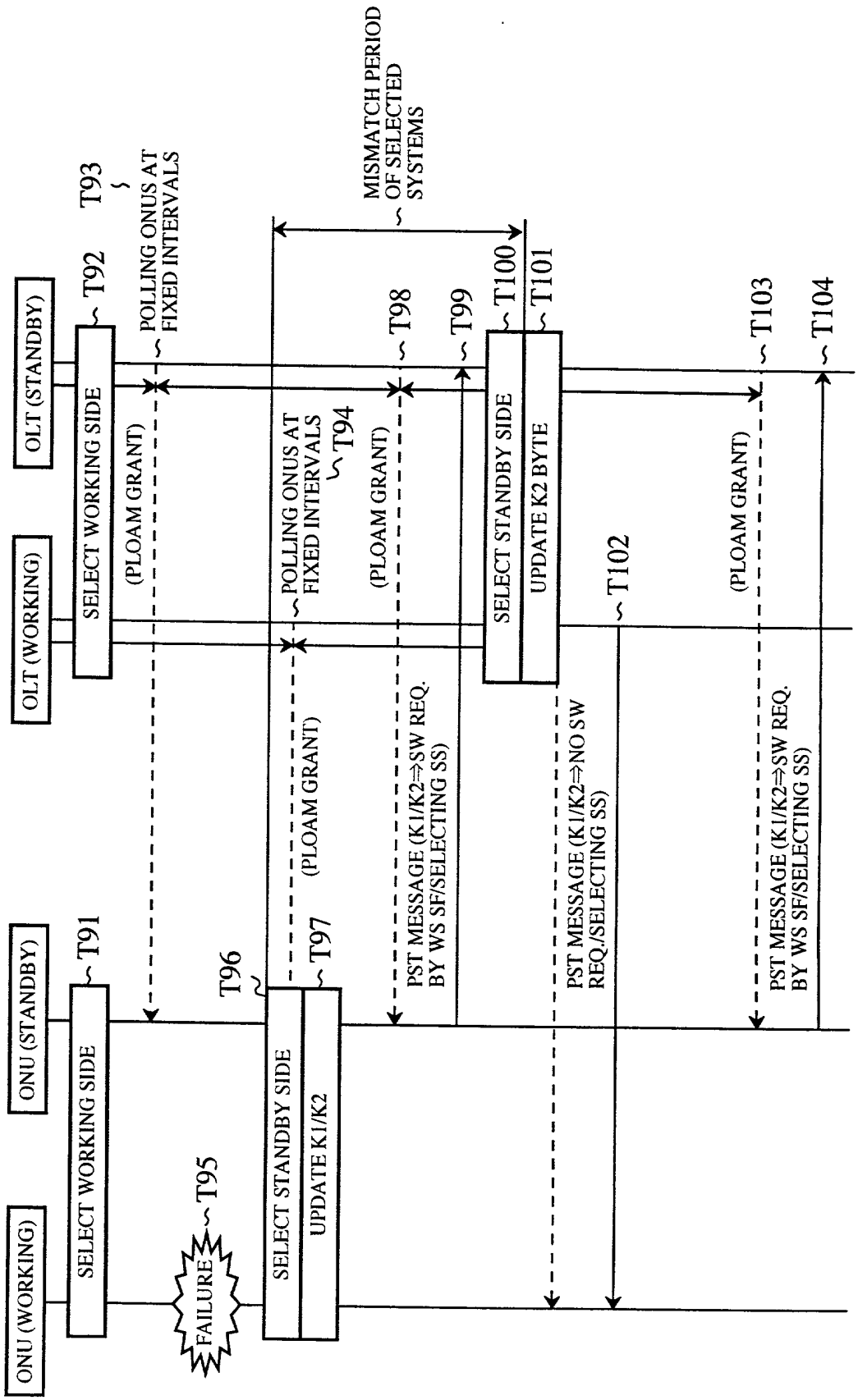
ONU (WORKING)
ONU (STANDBY)
OLT (WORKING)
OLT (STANDBY)
SELECT WORKING SIDE
SELECT STANDBY SIDE
UPDATE K1/K2
UPDATE K2 BYTE
FAILURE
T91
T92
T93
T94
T95
T96
T97
T98
T99
T100
T101
T102
T103
T104
POLLING ONUS AT FIXED INTERVALS
(PLOAM GRANT)
MISMATCH PERIOD OF SELECTED SYSTEMS
PST MESSAGE (K1/K2⇒SW REQ. BY WS SF/SELECTING SS)
PST MESSAGE (K1/K2⇒NO SW REQ./SELECTING SS)

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FIG.7

CONTROL EXAMPLE (REVERTIVE MODE)

FAULT STATE	ONU TO OLT		OLT TO ONU		OPERATION		
	K1 BYTE	K2 BYTE	K1 BYTE	K2 BYTE	ONU	OLT	
NO FAILURE	NO SW REQ.	(ONU IS) SELECTING WS	NO SW REQ.	(OLT IS) SELECTING WS	SL IS OPERATING IN WS	SL IS WORKING AT WS	~ S11
EQUIPMENT FAILURE OCCURS IN WS TX/RX OF ONU	SW REQ. BY WORKING SF	(ONU IS) SELECTING WS	NO SW REQ.	(OLT IS) SELECTING WS	DETECT SW REQ. BY WORKING SF; SL IS WORKING AT WS; UPDATE T-K1 BYTE	SL IS WORKING AT WS	~ S12
	SW REQ. BY WORKING SF	(ONU IS) SELECTING WS	NO SW REQ.	(OLT IS) SELECTING SS	DETECT SW REQ. BY WORKING SF; SL IS WORKING AT WS; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO SS; UPDATE T-K2 BYTE	~ S13
	SW REQ. BY WORKING SF	(ONU IS) SELECTING SS	NO SW REQ.	(OLT IS) SELECTING SS	DETECT RR BY RECEIVING K2; SL IS SWITCHED TO SS; UPDATE T-K2 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO SS; UPDATE T-K2 BYTE	~ S14
EQUIPMENT FAILURE IS RESTORED IN WS TX/RX OF ONU	REQUESTING TO REVERT TO WS	(ONU IS) SELECTING SS	NO SW REQ.	(OLT IS) SELECTING SS	DETECT SW REQ. CLEAR; WAIT TO REVERT STATE; UPDATE T-K1 BYTE	START RESTORE TIMER; SL IS OPERATING AT SS	~ S21
REVERSION WAITING EXPIRES AT OLT	WAITING TO REVERT TO WS	(ONU IS) SELECTING SS	NO SW REQ.	(OLT IS) SELECTING WS	DETECT SW REQ. CLEAR; WAIT TO REVERT STATE; UPDATE T-K1 BYTE	STOP RESTORE TIMER; SL IS SWITCHED TO WS; UPDATE T-K2 BYTE	~ S22
	NO SW REQ.	(ONU IS) SELECTING WS	NO SW REQ.	(OLT IS) SELECTING WS	DETECT RR BY RECEIVING K2; SL IS SWITCHED TO WS; UPDATE K1 BYTE	STOP RESTORE TIMER; SL IS SWITCHED TO WS; UPDATE T-K2 BYTE	~ S23

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FIG.8

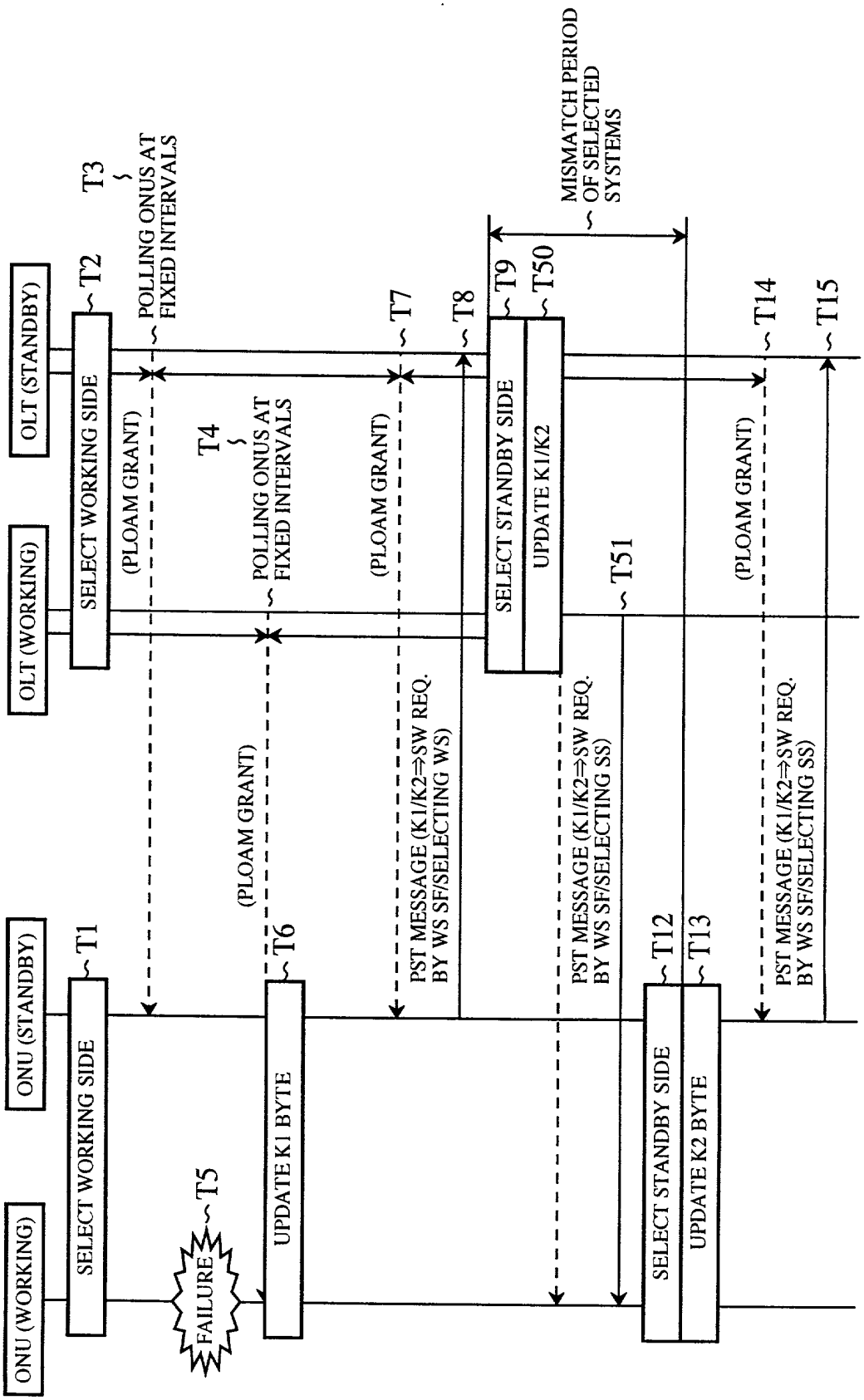
ONU (WORKING)
ONU (STANDBY)
OLT (WORKING)
OLT (STANDBY)
SELECT WORKING SIDE
SELECT WORKING SIDE
SELECT STANDBY SIDE
SELECT STANDBY SIDE
UPDATE K1 BYTE
UPDATE K1/K2
UPDATE K2 BYTE
FAILURE
T1
T2
T3
T4
T5
T6
T7
T8
T9
T50
T51
T12
T13
T14
T15
POLLING ONUS AT FIXED INTERVALS
POLLING ONUS AT FIXED INTERVALS
MISMATCH PERIOD OF SELECTED SYSTEMS
(PLOAM GRANT)
(PLOAM GRANT)
(PLOAM GRANT)
(PLOAM GRANT)
PST MESSAGE (K1/K2⇒SW REQ. BY WS SF/SELECTING WS)
PST MESSAGE (K1/K2⇒SW REQ. BY WS SF/SELECTING SS)
PST MESSAGE (K1/K2⇒SW REQ. BY WS SF/SELECTING SS)

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FIG.9

CONTROL EXAMPLE (NON-REVERTIVE MODE)

FAULT STATE	ONU TO OLT		OLT TO ONU		OPERATION		
	K1 BYTE	K2 BYTE	K1 BYTE	K2 BYTE	ONU	OLT	
NO FAILURE	NO SW REQ.	(ONU IS) SELECTING WS	NO SW REQ.	(OLT IS) SELECTING WS	SL IS OPERATING IN WS	SL IS WORKING AT WS	~ S11
EQUIPMENT FAILURE OCCURS IN WS TX/RX OF ONU	SW REQ. BY WORKING SF	(ONU IS) SELECTING WS	NO SW REQ.	(OLT IS) SELECTING WS	DETECT SW REQ. BY WORKING SF; SL IS WORKING AT WS; UPDATE T-K1 BYTE	SL IS WORKING AT WS	~ S12
	SW REQ. BY WORKING SF	(ONU IS) SELECTING WS	SW REQ. BY WORKING SF	(OLT IS) SELECTING SS	DETECT SW REQ. BY WORKING SF; SL IS WORKING AT WS; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	~ S13'
	SW REQ. BY WORKING SF	(ONU IS) SELECTING SS	SW REQ. BY WORKING SF	(OLT IS) SELECTING SS	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO SS; UPDATE T-K2 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	~ S14'
EQUIPMENT FAILURE IS RESTORED IN WS TX/RX OF ONU	DO NOT REVERT TO WS	(ONU IS) SELECTING SS	SW REQ. BY WORKING SF	(OLT IS) SELECTING SS	DETECT SW REQ. CLEAR; UPDATE T-K1 BYTE; DO NOT REVERT STATE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	~ S15'
	DO NOT REVERT TO WS	(ONU IS) SELECTING SS	DO NOT REVERT TO WS	(OLT IS) SELECTING SS	DETECT SW REQ. CLEAR; UPDATE T-K1 BYTE; DO NOT REVERT STATE	DETECT RR RELEASE BY RECEIVING K1/K2; DO NOT REVERT STATE; UPDATE T-K1 BYTE	~ S15"
SIGNAL DEGRADE OCCURS IN SS TX/RX OF ONU	SW REQ. BY STANDBY SD	(ONU IS) SELECTING SS	DO NOT REVERT TO WS	(OLT IS) SELECTING SS	DETECT SW REQ. BY WS SD; SL IS OPERATING AT SS; UPDATE T-K1 BYTE	DETECT RR RELEASE BY RECEIVING K1/K2; DO NOT REVERT STATE; UPDATE T-K1 BYTE	~ S16'

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FIG.10

SIGNAL DEGRADE OCCURS IN SS TX/RX OF ONU	SW REQ. BY STANDBY SD	(ONU IS) SELECTING SS	SW REQ. BY STANDBY SD	(OLT IS) SELECTING WS	DETECT SW REQ. BY WS SD; SL IS OPERATING AT SS; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO WS; UPDATE T-K1/K2 BYTES	~ S17'
	SW REQ. BY STANDBY SD	(ONU IS) SELECTING WS	SW REQ. BY STANDBY SD	(OLT IS) SELECTING WS	DETECT RR BY RECEIVING K1 AND K2; SL SWITCHED TO WS; UPDATE T-K2 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO WS; UPDATE T-K1/K2 BYTES	~ S18'
SIGNAL DEGRADE IS RESTORED IN SS TX/RX OF ONU	NO SW REQ.	(ONU IS) SELECTING WS	SW REQ. BY STANDBY SD	(OLT IS) SELECTING WS	NO REQ.; UPDATE K1 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO WS; UPDATE T-K1/K2 BYTES	~ S19'
	NO SW REQ.	(ONU IS) SELECTING WS	NO SW REQ.	(OLT IS) SELECTING WS	NO REQ.; UPDATE K1 BYTE	NO REQ.; UPDATE K1 BYTE	~ S19"

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FIG.11

CONTROL EXAMPLE (REVERTIVE MODE)

FAULT STATE	ONU TO OLT		OLT TO ONU		OPERATION		
	K1 BYTE	K2 BYTE	K1 BYTE	K2 BYTE	ONU	OLT	
NO FAILURE	NO SW REQ.	(ONU IS) SELECTING WS	NO SW REQ.	(OLT IS) SELECTING WS	SL IS OPERATING IN WS	SL IS WORKING AT WS	S11
EQUIPMENT FAILURE OCCURS IN WS TX/RX OF ONU	SW REQ. BY WORKING SF	(ONU IS) SELECTING WS	NO SW REQ.	(OLT IS) SELECTING WS	DETECT SW REQ. BY WORKING SF; SL IS WORKING AT WS; UPDATE T-K1 BYTE	SL IS WORKING AT WS	S12
	SW REQ. BY WORKING SF	(ONU IS) SELECTING WS	SW REQ. BY WORKING SF	(OLT IS) SELECTING SS	DETECT SW REQ. BY WORKING SF; SL IS WORKING AT WS; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	S13'
	SW REQ. BY WORKING SF	(ONU IS) SELECTING SS	SW REQ. BY WORKING SF	(OLT IS) SELECTING SS	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO SS; UPDATE T-K2 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	S14'
EQUIPMENT FAILURE IS RESTORED IN WS TX/RX OF ONU	REQUESTING TO REVERT TO WS	(ONU IS) SELECTING SS	SW REQ. BY WORKING SF	(OLT IS) SELECTING SS	DETECT SW REQ. CLEAR; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	S21'
	REQUESTING TO REVERT TO WS	(ONU IS) SELECTING SS	REQUESTING TO REVERT TO WS	(OLT IS) SELECTING SS	DETECT SW REQ. CLEAR; UPDATE T-K1 BYTE	DETECT RR RELEASE BY RECEIVING K1/K2; WAIT TO REVERT STATE; START RESTORE TIMER; UPDATE T-K1 BYTE	S21"

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FIG.12

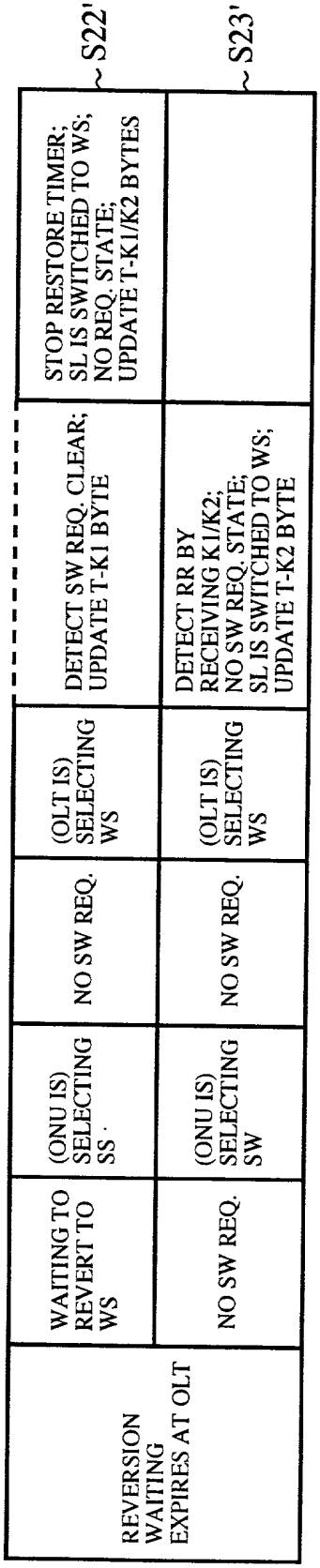
REVERSION WAITING EXPIRES AT OLT
WAITING TO REVERT TO WS
NO SW REQ.
(ONU IS) SELECTING SS
(ONU IS) SELECTING SW
NO SW REQ.
NO SW REQ.
(OLT IS) SELECTING WS
(OLT IS) SELECTING WS
DETECT SW REQ. CLEAR; UPDATE T-K1 BYTE
DETECT RR BY RECEIVING K1/K2; NO SW REQ. STATE; SL IS SWITCHED TO WS; UPDATE T-K2 BYTE
STOP RESTORE TIMER; SL IS SWITCHED TO WS; NO REQ. STATE; UPDATE T-K1/K2 BYTES
S22'
S23'

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FIG.13

FAULT STATE	ONU TO OLT		OLT TO ONU		OPERATION		
	K1 BYTE	K2 BYTE	K1 BYTE	K2 BYTE	ONU	OLT	
NO FAILURE	NO SW REQ.	(ONU) ESTABLISHES ITS ROUTE TO WS	NO SW REQ.	(OLT) ESTABLISHES ITS ROUTE TO WS	ROUTER AND SL ARE OPERATING AT WS	ROUTER AND SL ARE OPERATING AT WS	S31
EQUIPMENT FAILURE OCCURS IN WS TX/RX OF ONU	SW REQ. BY WORKING SF	(ONU) ESTABLISHES ITS ROUTE TO WS	NO SW REQ.	(OLT) ESTABLISHES ITS ROUTE TO WS	DETECT SW REQ. BY WORKING SF; SL IS WORKING AT WS; UPDATE T-K1 BYTE	ROUTER AND SL ARE OPERATING AT WS	S32
	SW REQ. BY WORKING SF	(ONU) ESTABLISHES ITS ROUTE TO WS	ACK	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT SW REQ. BY WORKING SF; SL IS WORKING AT WS; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; ROUTER IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	S33
	SW REQ. BY WORKING SF	(ONU) ESTABLISHES ITS ROUTE TO SS	ACK	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS; ROUTER IS SWITCHED TO SS; UPDATE T-K2 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; ROUTER IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	S34
	SW REQ. BY WORKING SF	(ONU) ESTABLISHES ITS ROUTE TO SS	ACK	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS; ROUTER IS SWITCHED TO SS; UPDATE T-K2 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS	S35
EQUIPMENT FAILURE IS RESTORED IN WS TX/RX OF ONU	DO NOT REVERT TO WS	(ONU) ESTABLISHES ITS ROUTE TO SS	ACK	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT SW REQ. CLEAR; DO NOT REVERT STATE; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS	S36

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FIG.14

SIGNAL DEGRADE OCCURS IN SS TX/RX OF ONU	SW REQ. BY STANDBY SD	(ONU) ESTABLISHES ITS ROUTE TO SS	NO SW REQ.	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT SW REQ. BY WS SD; SL IS OPERATING AT SS; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS	S37
	SW REQ. BY STANDBY SD	((ONU) ESTABLISHES ITS ROUTE TO SS	ACK	(OLT) ESTABLISHES ITS ROUTE TO WS	DETECT SW REQ. BY WS SD; SL IS OPERATING AT SS; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; ROUTER IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	S38
	SW REQ. BY STANDBY SD	(ONU) ESTABLISHES ITS ROUTE TO WS	ACK	(OLT) ESTABLISHES ITS ROUTE TO WS	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS; ROUTER IS SWITCHED TO SS; UPDATE T-K2 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; ROUTER IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	S39
	SW REQ. BY STANDBY SD	(ONU) ESTABLISHES ITS ROUTE TO WS	ACK	(OLT) ESTABLISHES ITS ROUTE TO WS	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS; ROUTER IS SWITCHED TO SS; UPDATE T-K2 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS	S40
SIGNAL DEGRADE IS RESTORED IN SS TX/RX OF ONU	NO SW REQ.	(ONU) ESTABLISHES ITS ROUTE TO WS	ACK	(OLT) ESTABLISHES ITS ROUTE TO WS	NO REQ.; UPDATE K1 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS	S41
	NO SW REQ.	(ONU) ESTABLISHE S ITS ROUTE TO WS	NO SW REQ.	(OLT) ESTABLISHES ITS ROUTE TO WS	NO REQ.; UPDATE K1 BYTE	NO REQ.; UPDATE K1 BYTE	S42

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FIG.15

FAULT STATE	ONU TO OLT		OLT TO ONU		OPERATION		
	K1 BYTE	K2 BYTE	K1 BYTE	K2 BYTE	ONU	OLT	
NO FAILURE	NO SW REQ.	(ONU) ESTABLISHES ITS ROUTE TO WS	NO SW REQ.	(OLT) ESTABLISHES ITS ROUTE TO WS	ROUTER AND SL ARE OPERATING AT WS	ROUTER AND SL ARE OPERATING AT WS	S31
EQUIPMENT FAILURE OCCURS IN WS TX/RX OF ONU	SW REQ. BY WORKING SF	(ONU) ESTABLISHES ITS ROUTE TO WS	NO SW REQ.	(OLT) ESTABLISHES ITS ROUTE TO WS	DETECT SW REQ. BY WORKING SF; SL IS WORKING AT WS; UPDATE T-K1 BYTE	ROUTER AND SL ARE OPERATING AT WS	S32
	SW REQ. BY WORKING SF	(ONU) ESTABLISHES ITS ROUTE TO WS	ACK	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT SW REQ. BY WORKING SF; SL IS WORKING AT WS; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; ROUTER IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	S33
	SW REQ. BY WORKING SF	(ONU) ESTABLISHES ITS ROUTE TO SS	ACK	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS; ROUTER IS SWITCHED TO SS; UPDATE T-K2 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; ROUTER IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	S34
	SW REQ. BY WORKING SF	(ONU) ESTABLISHES ITS ROUTE TO SS	ACK	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS; ROUTER IS SWITCHED TO SS; UPDATE T-K2 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS	S35
EQUIPMENT FAILURE IS RESTORED IN WS TX/RX OF ONU	REQUESTING TO REVERT TO WS	(ONU) ESTABLISHES ITS ROUTE TO SS	ACK	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT SW REQ. CLEAR; WAIT TO REVERT STATE; UPDATE T-K1 BYTE	START RESTORE TIMER; SL IS OPERATING AT SS	S51

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FIG.16

REVERSION WAITING EXPIRES AT OLT	WAITING TO REVERT TO WS	(ONU) ESTABLISHES ITS ROUTE TO SS	NO SW REQ.	(OLT) ESTABLISHES ITS ROUTE TO WS	DETECT SW REQ. CLEAR; WAIT TO REVERT STATE; UPDATE T-K1 BYTE	STOP RESTORE TIMER; ROUTER IS SWITCHED TO WS; NO SW REQ. STATE; UPDATE T-K1/K2 BYTES	S52
	NO SW REQ.	(ONU) ESTABLISHES ITS ROUTE TO WS	NO SW REQ.	(OLT) ESTABLISHES ITS ROUTE TO WS	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO WS; ROUTER IS SWITCHED TO WS; UPDATE T-K1/K2 BYTES	STOP RESTORE TIMER; ROUTER IS SWITCHED TO WS; NO SW REQ. STATE; UPDATE T-K1/K2 BYTES	S53
	NO SW REQ.	(ONU) ESTABLISHES ITS ROUTE TO WS	NO SW REQ.	(OLT) ESTABLISHES ITS ROUTE TO WS	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO WS; ROUTER IS SWITCHED TO WS; UPDATE T-K1/K2 BYTES	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO WS	S54

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FIG.17

FAULT STATE	ONU TO OLT		OLT TO ONU		OPERATION		
	K1 BYTE	K2 BYTE	K1 BYTE	K2 BYTE	ONU	OLT	
NO FAILURE	NO SW REQ.	(ONU) ESTABLISHES ITS ROUTE TO WS	NO SW REQ.	(OLT) ESTABLISHES ITS ROUTE TO WS	ROUTER AND SL ARE OPERATING AT WS	ROUTER AND SL ARE OPERATING AT WS	S31
EQUIPMENT FAILURE OCCURS IN WS TX/RX OF ONU	SW REQ. BY WORKING SF	(ONU) ESTABLISHES ITS ROUTE TO WS	NO SW REQ.	(OLT) ESTABLISHES ITS ROUTE TO WS	DETECT SW REQ. BY WORKING SF; SL IS WORKING AT WS; UPDATE T-K1 BYTE	ROUTER AND SL ARE OPERATING AT WS	S32
	SW REQ. BY WORKING SF	(ONU) ESTABLISHES ITS ROUTE TO WS	ACK	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT SW REQ. BY WORKING SF; SL IS WORKING AT WS; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; ROUTER IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	S33
	SW REQ. BY WORKING SF	(ONU) ESTABLISHES ITS ROUTE TO SS	ACK	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS; ROUTER IS SWITCHED TO SS; UPDATE T-K2 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; ROUTER IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	S34
	SW REQ. BY WORKING SF	(ONU) ESTABLISHES ITS ROUTE TO SS	ACK	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS; ROUTER IS SWITCHED TO SS; UPDATE T-K2 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS	S35
EQUIPMENT FAILURE IS RESTORED IN WS TX/RX OF ONU	DO NOT REVERT TO WS	(ONU) ESTABLISHES ITS ROUTE TO SS	ACK	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT SW REQ. CLEAR; DO NOT REVERT STATE; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS	S36
	DO NOT REVERT TO WS	(ONU) ESTABLISHES ITS ROUTE TO SS	DO NOT REVERT TO WS	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT SW REQ. CLEAR; DO NOT REVERT STATE; UPDATE T-K1 BYTE	DETECT RR RELEASE BY RECEIVING K1/K2 BYTES; DO NOT REVERT STATE; UPDATE T-K1 BYTE	S36'

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FIG.18

SIGNAL DEGRADE OCCURS IN SS TX/RX OF ONU	SW REQ. BY STANDBY SD	(ONU) ESTABLISHES ITS ROUTE TO SS	DO NOT REVERT TO WS	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT SW REQ. BY STANDBY SD; SL IS OPERATING AT SS; UPDATE T-K1 BYTE	DETECT RR RELEASE BY RECEIVING K1/K2 BYTES; DO NOT REVERT STATE; UPDATE T-K1 BYTE	S37'
	SW REQ. BY STANDBY SD	((ONU) ESTABLISHES ITS ROUTE TO SS	ACK	(OLT) ESTABLISHES ITS ROUTE TO WS	DETECT SW REQ. BY STANDBY SD; SL IS OPERATING AT SS; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; ROUTER IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	S38
	SW REQ. BY STANDBY SD	(ONU) ESTABLISHES ITS ROUTE TO WS	ACK	(OLT) ESTABLISHES ITS ROUTE TO WS	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS; ROUTER IS SWITCHED TO SS; UPDATE T-K2 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; ROUTER IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	S39
	SW REQ. BY STANDBY SD	(ONU) ESTABLISHES ITS ROUTE TO WS	ACK	(OLT) ESTABLISHES ITS ROUTE TO WS	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS; ROUTER IS SWITCHED TO SS; UPDATE T-K2 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS	S40
SIGNAL DEGRADE IS RESTORED IN SS TX/RX OF ONU	NO SW REQ.	(ONU) ESTABLISHES ITS ROUTE TO WS	ACK	(OLT) ESTABLISHES ITS ROUTE TO WS	NO REQ.; UPDATE K1 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS	S41
	NO SW REQ.	(ONU) ESTABLISHE S ITS ROUTE TO WS	NO SW REQ.	(OLT) ESTABLISHES ITS ROUTE TO WS	NO REQ.; UPDATE K1 BYTE	NO REQ.; UPDATE K1 BYTE	S42

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FIG.19

FAULT STATE	ONU TO OLT		OLT TO ONU		OPERATION		
	K1 BYTE	K2 BYTE	K1 BYTE	K2 BYTE	ONU	OLT	
NO FAILURE	NO SW REQ.	(ONU) ESTABLISHES ITS ROUTE TO WS	NO SW REQ.	(OLT) ESTABLISHES ITS ROUTE TO WS	ROUTER AND SL ARE OPERATING AT WS	ROUTER AND SL ARE OPERATING AT WS	S31
EQUIPMENT FAILURE OCCURS IN WS TX/RX OF ONU	SW REQ. BY WORKING SF	(ONU) ESTABLISHES ITS ROUTE TO WS	NO SW REQ.	(OLT) ESTABLISHES ITS ROUTE TO WS	DETECT SW REQ. BY WORKING SF; SL IS WORKING AT WS; UPDATE T-K1 BYTE	ROUTER AND SL ARE OPERATING AT WS	S32
	SW REQ. BY WORKING SF	(ONU) ESTABLISHES ITS ROUTE TO WS	ACK	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT SW REQ. BY WORKING SF; SL IS WORKING AT WS; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; ROUTER IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	S33
	SW REQ. BY WORKING SF	(ONU) ESTABLISHES ITS ROUTE TO SS	ACK	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS; ROUTER IS SWITCHED TO SS; UPDATE T-K2 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; ROUTER IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	S34
	SW REQ. BY WORKING SF	(ONU) ESTABLISHES ITS ROUTE TO SS	ACK	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS; ROUTER IS SWITCHED TO SS; UPDATE T-K2 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS	S35
EQUIPMENT FAILURE IN WS RX OF ONU IS RESTORED	REQUESTING TO REVERT TO WS.	(ONU) ESTABLISHES ITS ROUTE TO SS	ACK	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT SW REQ. CLEAR; WAIT TO REVERT STATE; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO SS	S51
	REQUESTING TO REVERT TO WS.	(ONU) ESTABLISHES ITS ROUTE TO SS	REQUESTING TO REVERT TO WS	(OLT) ESTABLISHES ITS ROUTE TO SS	DETECT SW REQ. CLEAR; WAIT TO REVERT STATE; UPDATE T-K1 BYTE	DETECT RR RELEASE BY RECEIVING K1/K2 BYTES; WAIT TO REVERT STATE; START RESTORE TIMER; UPDATE T-K1 BYTE	S51'

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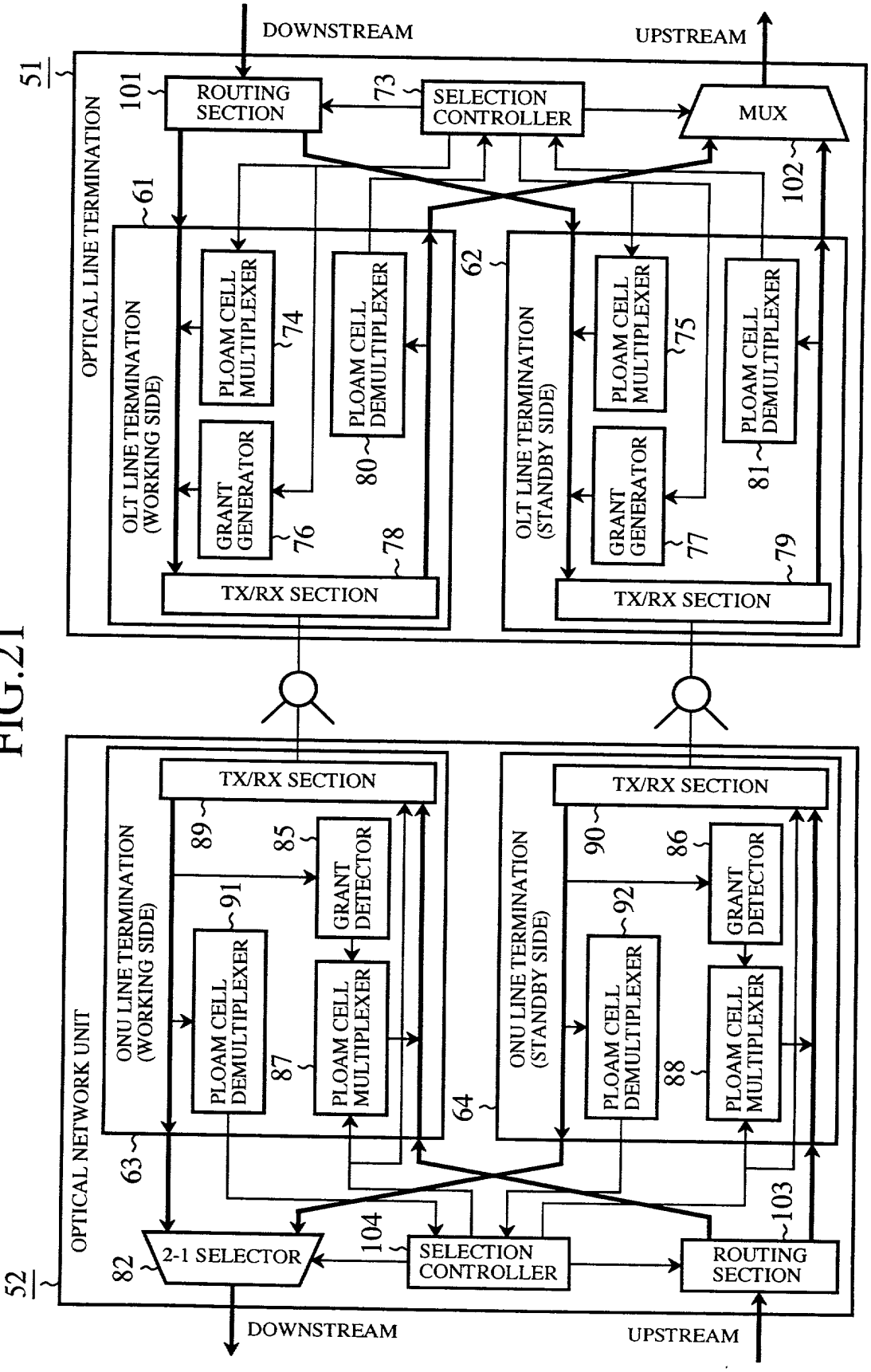
FIG.20

REVERSION WAITING EXPIRES AT OLT	WAITING TO REVERT TO WS	(ONU) ESTABLISHES ITS ROUTE TO SS	NO SW REQ.	(OLT) ESTABLISHES ITS ROUTE TO WS	DETECT SW REQ. CLEAR; WAIT TO REVERT STATE; UPDATE T-K1 BYTE	STOP RESTORE TIMER; ROUTER IS SWITCHED TO WS; NO SW REQ. STATE; UPDATE T-K1/K2 BYTES	S52
	NO SW REQ.	(ONU) ESTABLISHES ITS ROUTE TO WS	NO SW REQ.	(OLT) ESTABLISHES ITS ROUTE TO WS	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO WS; ROUTER IS SWITCHED TO WS; UPDATE T-K1/K2 BYTES	STOP RESTORE TIMER; ROUTER IS SWITCHED TO WS; NO SW REQ. STATE; UPDATE T-K1/K2 BYTES	S53
	NO SW REQ.	(ONU) ESTABLISHES ITS ROUTE TO WS	NO SW REQ.	(OLT) ESTABLISHES ITS ROUTE TO WS	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO WS; ROUTER IS SWITCHED TO WS; UPDATE T-K1/K2 BYTES	DETECT RR BY RECEIVING K1/K2 BYTES; SL IS SWITCHED TO WS	S54

FIG.21


DOWNSTREAM
UPSTREAM
51
OPTICAL LINE TERMINATION
101
ROUTING SECTION
73
SELECTION CONTROLLER
MUX
102
61
OLT LINE TERMINATION (WORKING SIDE)
PLOAM CELL MULTIPLEXER
74
PLOAM CELL DEMULTIPLEXER
80
GRANT GENERATOR
76
78
TX/RX SECTION
62
OLT LINE TERMINATION (STANDBY SIDE)
PLOAM CELL MULTIPLEXER
75
PLOAM CELL DEMULTIPLEXER
81
GRANT GENERATOR
77
79
TX/RX SECTION
52
OPTICAL NETWORK UNIT
ONU LINE TERMINATION (WORKING SIDE)
TX/RX SECTION
89
85
GRANT DETECTOR
91
PLOAM CELL DEMULTIPLEXER
87
PLOAM CELL MULTIPLEXER
63
64
ONU LINE TERMINATION (STANDBY SIDE)
TX/RX SECTION
90
86
GRANT DETECTOR
92
PLOAM CELL DEMULTIPLEXER
88
PLOAM CELL MULTIPLEXER
103
82
2-1 SELECTOR
104
SELECTION CONTROLLER
ROUTING SECTION
DOWNSTREAM
UPSTREAM

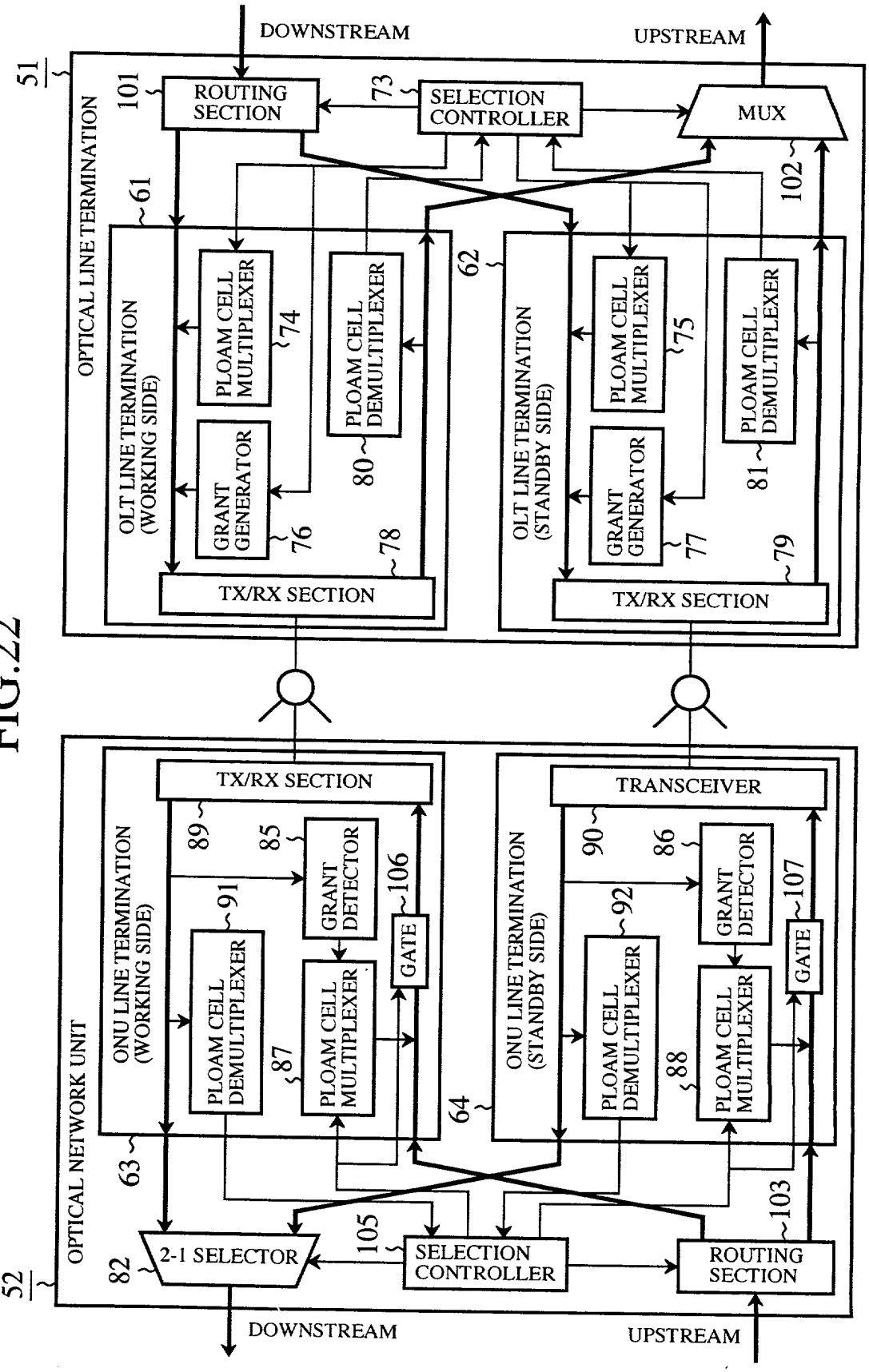

DOWNSTREAM
UPSTREAM
51
OPTICAL LINE TERMINATION
101
ROUTING SECTION
73
SELECTION CONTROLLER
MUX
102
61
OLT LINE TERMINATION (WORKING SIDE)
PLOAM CELL MULTIPLEXER
74
PLOAM CELL DEMULTIPLEXER
GRANT GENERATOR
76
80
78
TX/RX SECTION
62
OLT LINE TERMINATION (STANDBY SIDE)
PLOAM CELL MULTIPLEXER
75
PLOAM CELL DEMULTIPLEXER
GRANT GENERATOR
77
81
79
TX/RX SECTION
52
OPTICAL NETWORK UNIT
ONU LINE TERMINATION (WORKING SIDE)
TX/RX SECTION
89
85
GRANT DETECTOR
106
GATE
91
PLOAM CELL DEMULTIPLEXER
87
PLOAM CELL MULTIPLEXER
63
64
ONU LINE TERMINATION (STANDBY SIDE)
TRANSCEIVER
90
86
GRANT DETECTOR
107
GATE
92
PLOAM CELL DEMULTIPLEXER
88
PLOAM CELL MULTIPLEXER
103
82
2-1 SELECTOR
105
SELECTION CONTROLLER
ROUTING SECTION
DOWNSTREAM
UPSTREAM

FIG.22

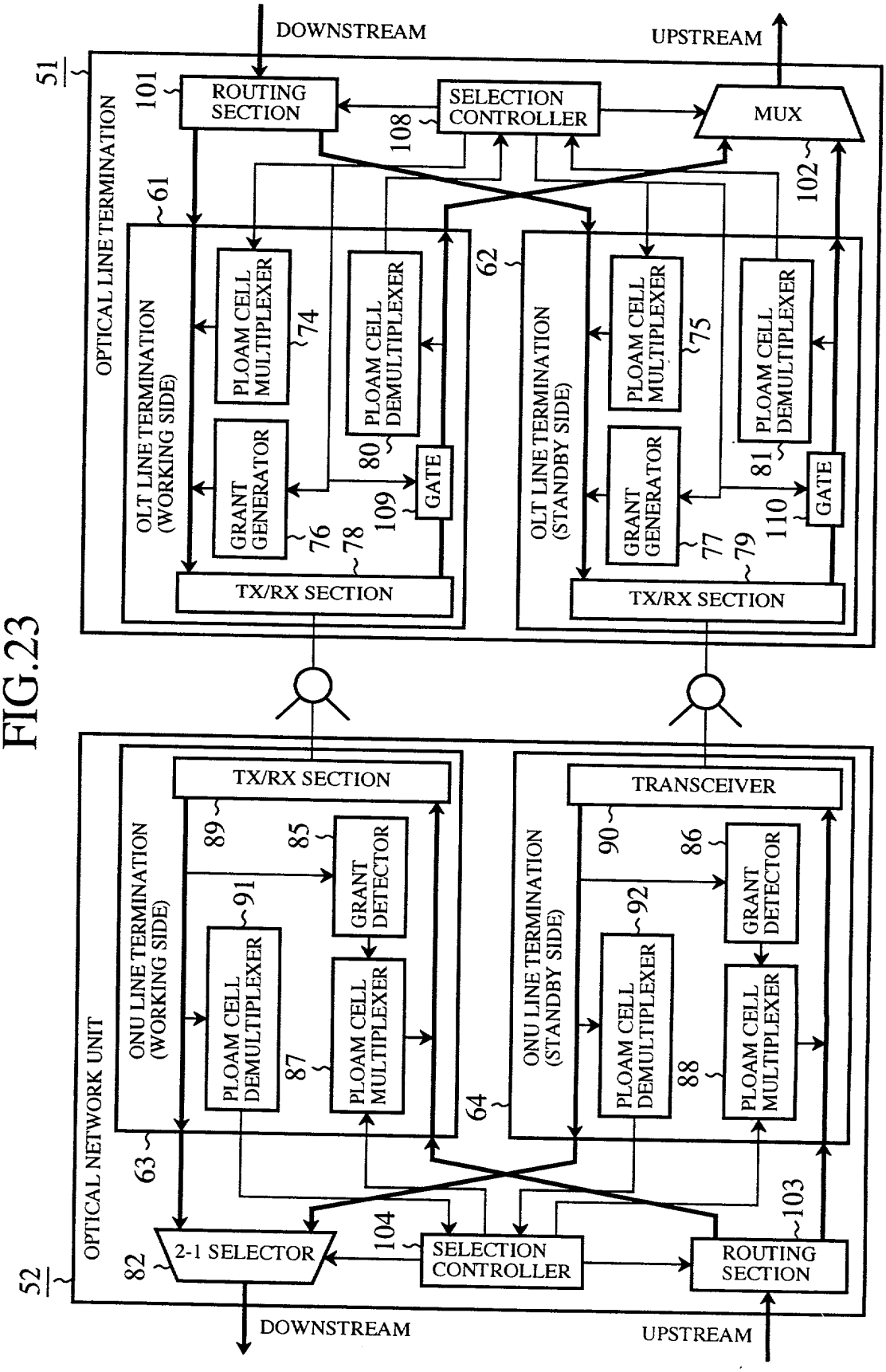

DOWNSTREAM
UPSTREAM
51
OPTICAL LINE TERMINATION
101
ROUTING SECTION
SELECTION CONTROLLER
108
MUX
102
61
OLT LINE TERMINATION (WORKING SIDE)
PLOAM CELL MULTIPLEXER
74
PLOAM CELL DEMULTIPLEXER
GRANT GENERATOR
80
GATE
109
76
78
TX/RX SECTION
62
OLT LINE TERMINATION (STANDBY SIDE)
PLOAM CELL MULTIPLEXER
75
PLOAM CELL DEMULTIPLEXER
GRANT GENERATOR
81
GATE
110
77
79
TX/RX SECTION
52
OPTICAL NETWORK UNIT
63
ONU LINE TERMINATION (WORKING SIDE)
TX/RX SECTION
89
85
GRANT DETECTOR
91
PLOAM CELL DEMULTIPLEXER
87
PLOAM CELL MULTIPLEXER
64
ONU LINE TERMINATION (STANDBY SIDE)
TRANSCEIVER
90
86
GRANT DETECTOR
92
PLOAM CELL DEMULTIPLEXER
88
PLOAM CELL MULTIPLEXER
82
2-1 SELECTOR
104
SELECTION CONTROLLER
103
ROUTING SECTION
DOWNSTREAM
UPSTREAM

FIG.23

FIG.24 (PRIOR ART)

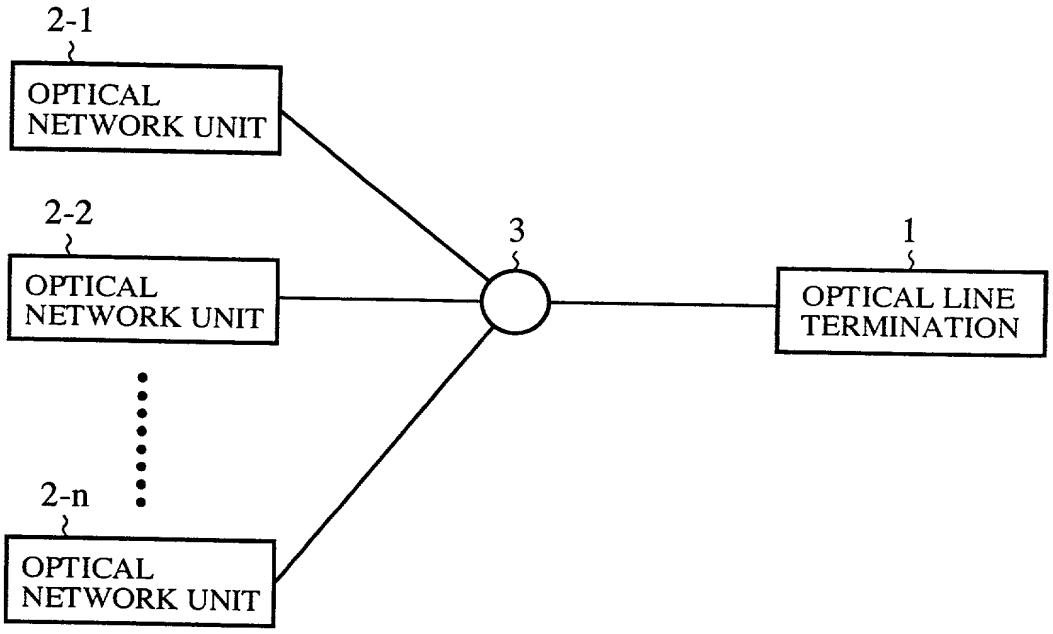
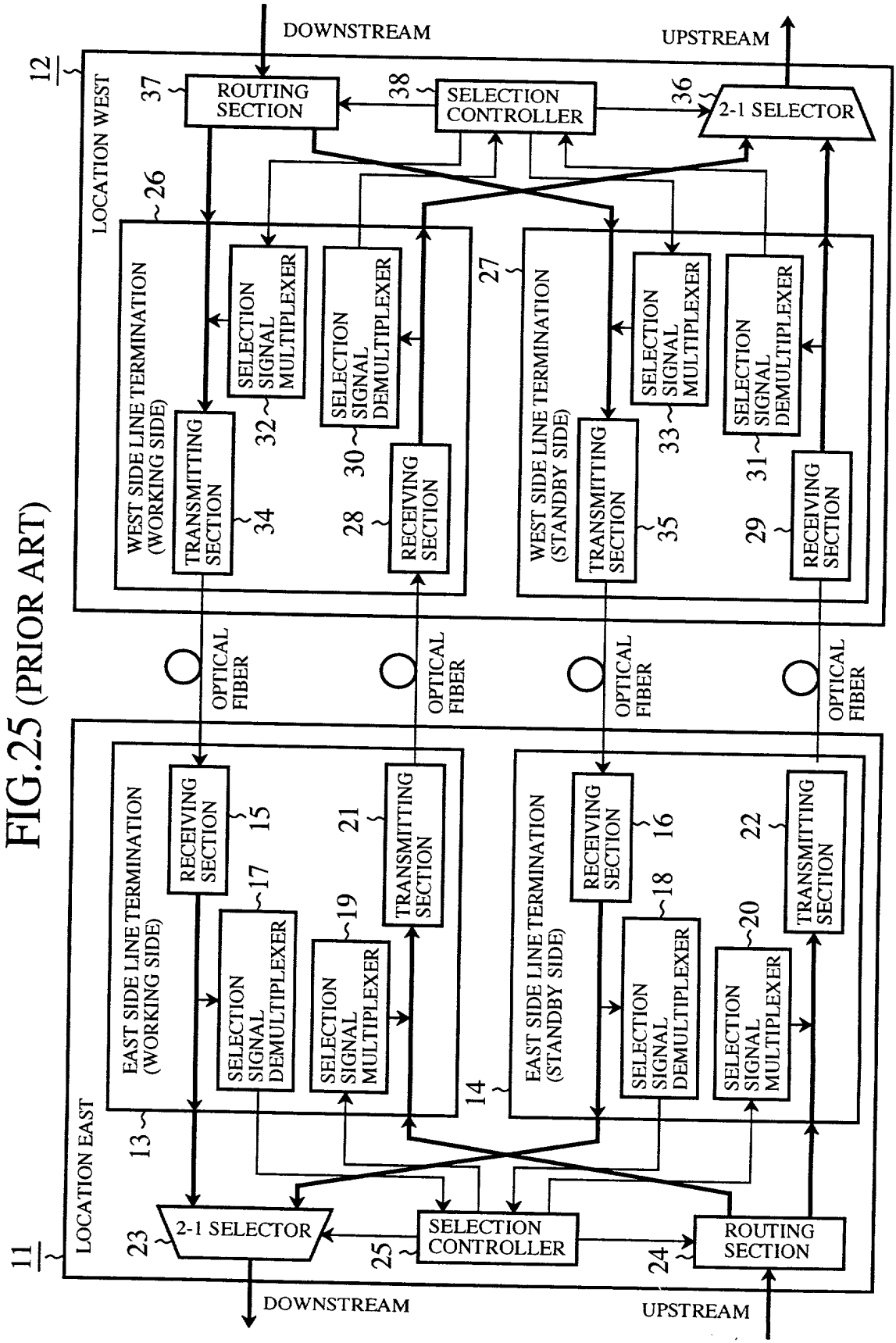


FIG.25 (PRIOR ART)



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CONTROL EXAMPLE (NON-REVERTIVE MODE)

FIG.26 (PRIOR ART)

FAULT STATE	LE TO LW		LW TO LE		OPERATION		
	K1 BYTE	K2 BYTE	K1 BYTE	K2 BYTE	LE	LW	
NO FAILURE	NO SW REQ.	(LE IS) SELECTING WS	NO SW REQ.	(LW IS) SELECTING WS	SL IS OPERATING IN WS	SL IS OPERATING IN WS	~S1
EQUIPMENT FAILURE OCCURS IN WS RX OF LE	SW REQ. BY WORKING SF	(LE IS) SELECTING SS	NO SW REQ.	(LW IS) SELECTING WS	DETECT SW REQ. BY WORKING SF; SL IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	SL IS OPERATING IN WS	~S2
	SW REQ. BY WORKING SF	(LE IS) SELECTING SS	NO SW REQ.	(LW IS) SELECTING SS	DETECT SW REQ. BY WORKING SF; SL IS SWITCHED TO SS; UPDATE T-K1/K2 BYTES	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO SS; UPDATE T-K2 BYTE	~S3
EQUIPMENT FAILURE IS RESTORED IN WS RX OF LE	DO NOT REVERT TO WS	(LE IS) SELECTING SS	NO SW REQ.	(LW IS) SELECTING SS	DETECT SW REQ. CLEAR; DO NOT REVERT STATE; UPDATE T-K1 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO SS; UPDATE T-K2 BYTE	~S4
SIGNAL DEGRADE OCCURS IN SS RX OF LE	SW REQ. BY STANDBY SD	(LE IS) SELECTING WS	NO SW REQ.	(LW IS) SELECTING SS	DETECT SW REQ. BY WORKING SD; SL IS SWITCHED TO WS; UPDATE T-K1/K2 BYTES	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO SS; UPDATE T-K2 BYTE	~S5
	SW REQ. BY STANDBY SD	(LE IS) SELECTING WS	NO SW REQ.	(LW IS) SELECTING WS	DETECT SW REQ. BY WORKING SD; SL IS SWITCHED TO WS; UPDATE T-K1/K2 BYTES	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO WS; UPDATE T-K2 BYTE	~S6
SIGNAL DEGRADE IS RESTORED IN SS RX OF LE	NO SW REQ.	(LE IS) SELECTING WS	NO SW REQ.	(LW IS) SELECTING WS	NO REQ.; UPDATE K1 BYTE	DETECT RR BY RECEIVING K1/K2; SL IS SWITCHED TO WS; UPDATE T-K2 BYTE	~S7

NOTE: LE=LOCATION EAST; LW=LOCATION WEST

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